

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination KITAJIMA ET AL.	
		Examiner Mary C Hogan	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,389,379	05-2002	Lin et al.	703/14
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U ✓	Lindgren et al, "Look-up-Table FPGA Synthesis from Minimized Multi-Valued Pseudo Kronecker Expressions" Proceedings of the 28th IEEE Symposium on Multiple-Valued Logic, pages 95-100, 1998
	V ✓	Yurchak et al, "HAMLET-An Expression Compiler/Optimizer for the Implementation of Heuristics to Minimize Multiple-Valued Programmable Logic Arrays", Proceedings of the 20th IEEE Symposium on Multiple-Valued Logic, pages 144-152, 1990
	W ✓	Sheikholeslami et al, "Look-Up Tables (LUTs) for Multiple-Valued, Combinational Logic", Proceedings of the 28th IEEE Symposium on Multiple-Valued Logic, pages 264-269, 1998
	X ✓	Miller, D. Michael, "Multiple-Valued Logic Design Tools", Proceedings of the 23rd IEEE Symposium on Multiple-Valued Logic, pages 24-27, 1993

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.